

Amendments to the Specification

Please amend the paragraph on page 6 at lines 15-27 of the Specification as follows:

The architecture of a computer system, which is indicated generally at 17, is shown in Figure 2. Computer system 17 includes a processor 19, which is coupled to a memory controller hub (MCH) 21. Memory controller hub 21 is coupled to a graphics controller 20 and system memory 22. Memory controller hub 21 is additionally coupled to an I/O controller hub (ICH) 24, which is coupled to a USB controller 25 and a PCI bus 27. I/O controller hub 24 is also coupled to BIOS 26, which is coupled via a communications link to the array controller of power supply array 10. BIOS 26 is able to assert certain signal line inputs to the processor through the I/O controller hub 24 and memory controller hub 21. With respect to certain processors manufactured by Intel® Corporation of Santa Clara, California, one such signal line input to the processor is the PROCHOT# (processor hot) input. The assertion of the PROCHOT signal line in certain Intel® processor, including at least some Intel® Pentium® 4 model processors, causes the processor to modulate its internal processor core clocks. The processor modulates its internal processor core clocks by turning the various clocks of the processor off and on according to a predetermined duty cycle, thereby reducing the effective rate of the clock.